



NOTES

1. PCB USES A PWM ON THE "SOLENOID-" OUTPUT
2. POWER REDUCTION TO 25%
3. IN A SHIELDED BOX TO REDUCE RFI ON SSB
4. EVERYTHING POTTED IN BOX
5. THE SPDT ELIMINATES THE CIRCUIT IN CASE OF PCB FAILURE
6. IF A PCB FAILURE, THE LED WILL TURN OFF
7. PRESSING THE SPDT REVERTS BACK TO FULL POWER TO SOLENOID
8. SPDT BUTTON: OUT-25% POWER; IN-100% POWER

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File: Hit and Hold.kicad_sch

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